

PATENT Docket No. SPLX.P0060

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Steven Teig, et al.

Serial No.:

09/681,776

Filing Date:

6/03/2001

For:

DIAGONAL WIRING ARHITECTURE

FOR INTEGRATED CIRCUIT

PATENT APPLICATION

Examiner:

<Unassigned>

Group Art Unit: 2811

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence is being deposited in the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Assistant Commissioner for Patents, Washington, D.C. 20231, on October 15, 2001.

John C. Stattler

Signature Date: October 15, 2001

PETITION FOR EXTENSION OF TIME UNDER 37 C.F.R. §1.136

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

Applicant petitions the Assistant Commissioner for Patents under 37 C.F.R. § 1.136(a), to extend the time for response to the Notice to File Missing Parts dated June, 15, 2001. This extension will extend the time the response is due from August 15, 2001 to October 15, 2001.

A two month extension is requested; the extension fee is \$400.00

A Credit Card Payment Form.

12/28/2001 SSANDARA 00000003 501128 09681776

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6.00 CH

394.00 OP

The Assistant Commissioner is hereby authorized to charge any additional fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by this petition, or to credit any overpayment to **Deposit Account No. 50 1128** referencing docket no. SPLX.P0060.

Respectfully submitted,

Dated: October 15, 2001

By

John Stattler

Registration No. 36,285

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